Mentor Graphics is one of the world leaders in the Electronic Design Automation (EDA) market. The company develops and markets hardware solutions (emulators) and software products covering the entire design flow of electronic systems and components.

Mentor Graphics's clients include companies in the fields of telecommunications, semiconductors, IT, consumer applications, automotive, and military and space.

**Context and job description**

The open position is in the Simulation Analog Engineering group in charge of the development of a new project in the field of fast circuit simulation. The person will contribute to the architecture, design, implementation and verification of the software in a team whose job is to develop a new generation of simulators. This includes drafting technical specifications, design documents, algorithmic descriptions and test plans. This work will be carried out in close collaboration with other teams abroad.

The position is based in Grenoble (France) in a very dynamic area, particularly in the semiconductor industry around ST Microelectronics, numerous research organizations (CEA / LETI, CNRS), Universities, Schools Engineers and a dynamic EDA ecosystem.

**Profile of the candidate**

At least 5 years’ experience in programming sophisticated algorithms in the field of simulation and / or signal processing. Candidates must be curious about:

- Develop expertise in algorithms used in circuit simulation and in particular for Radio Frequency (RF) analyzes,
- Understand how integrated circuits work.

He is rigorous and disciplined when developing software and will respect the development processes under software quality assurance.

Reactive, autonomous, creative and will be invested to become force of proposition.

The candidate has good communication skills and knows how to work as a team.

The practice of English (written and spoken) is necessary.

**Required Skills**

A solid programming experience in C / C++ on Linux

Resolution of nonlinear systems.

Linear algebra.

Signal processing and associated tools.

**Would be a plus:**

Training on analog circuit simulation algorithms or experience in the development of modern Spice simulators.

Knowledge of algorithmic / parallel programming.